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Applicant: Takeo Oita

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U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate

FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
u	2002-217686	Aug. 2, 2002	Japan			Abs.	
u	06-268442	Sept. 22, 1994	Japan			Abs.	
u	06-120734	April 28, 1994	Japan			Abs.	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

u	Takeshi Yoshida, "Designing Know-how of High Frequency Circuit," August 10, 1989.
u	Notice of Rejection Grounds for Japanese Patent Application No. 2001-007977, mailed September 7, 2005.

Examiner

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Date Considered

12/11/2005

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Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.